

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/697,079	10/31/2003	Manolito M. Catalasan	1875.4360002	9730	
26111	7590 08/10/200	i	EXAMINER		
STERNE, KESSLER, GOLDSTEIN & FOX PLLC			TRINH, HOA B		
	YORK AVENUE, N.W TON, DC 20005	•	ART UNIT	PAPER NUMBER	
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,		2814		
			DATE MAILED: 08/10/200	DATE MAILED: 08/10/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		W			
	Application No.	Applicant(s)			
	10/697,079	CATALASAN ET AL.			
Office Action Summary	Examiner	Art Unit			
	Vikki H. Trinh	2814			
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address			
A SHORTENED STATUTORY PERIOD FOR RE WHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CFF after SIX (6) MONTHS from the mailing date of this communication - If NO period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by stany reply received by the Office later than three months after the mearned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNI R 1.136(a). In no event, however, may a riod will apply and will expire SIX (6) MO atute, cause the application to become A	CATION. reply be timely filed NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on $\underline{0}$	<u>6 June 2006</u> .				
2a)⊠ This action is FINAL . 2b)□ 1	This action is FINAL . 2b) ☐ This action is non-final.				
3) Since this application is in condition for allo	·	·			
closed in accordance with the practice und	er <i>Ex parte Quayle</i> , 1935 C.I). 11, 453 O.G. 213.			
Disposition of Claims					
4) ⊠ Claim(s) 1-24 is/are pending in the applicat 4a) Of the above claim(s) 1-9 and 15-24 is/s 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 10-14 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and	are withdrawn from considera	ition.			
Application Papers					
9) The specification is objected to by the Exam 10) The drawing(s) filed on 31 October 2003 is/ Applicant may not request that any objection to Replacement drawing sheet(s) including the cor 11) The oath or declaration is objected to by the	are: a)⊠ accepted or b)⊡ o the drawing(s) be held in abeya rrection is required if the drawing	nce. See 37 CFR 1.85(a). I(s) is objected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of: 1. Certified copies of the priority docum 2. Certified copies of the priority docum 3. Copies of the certified copies of the papplication from the International But * See the attached detailed Office action for a	ents have been received. ents have been received in A priority documents have beer reau (PCT Rule 17.2(a)).	Application No received in this National Stage			
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No	Summary (PTO-413) s)/Mail Date			
Information Disclosure Statement(s) (PTO-1449 or PTO/SB. Paper No(s)/Mail Date	/08) 5) Notice of 6) Other:	nformal Patent Application (PTO-152) 			

Application/Control Number: 10/697,079 Page 2

Art Unit: 2814

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:
 - 1. Determining the scope and contents of the prior art.
 - 2. Ascertaining the differences between the prior art and the claims at issue.
 - 3. Resolving the level of ordinary skill in the pertinent art.
 - 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 3. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- 4. Claims 10-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Burgess et al. (5,408,428) (hereinafter Burgess).

Art Unit: 2814

As to claim 10, Burgess discloses an integrated circuit chip including a plurality of metal layers M1-M4 (fig. 3) and first and second supply potentials 28 VDD, 26 GND, respectively (fig. 9), a programmable memory cell 16 (fig. 3) for storing a plurality of values, the memory cell comprising a first metal interconnect structure 30 (fig. 3) that traverses the plurality of metal layers using a first plurality of vias 32, 36, 44, 40, 48, 52 (fig. 3); a second metal interconnect structure 38 (fig. 3) that traverses the plurality of metal layers using a second 38 plurality of vias (fig. 3), an output Vout 56 (fig 2 and fig. 3) coupled to the first and second supply potentials by at least one of said first and second metal interconnect structure, wherein each of said first and second metal interconnect structures can be programmed repeatedly by altering any one of the plurality of metal layers and any one of a plurality of via layers (figs. 3-7). However, Burgess does not explicitly teach more than one output. Still, it would have been an obvious matter of choice bounded by well known manufacturing constraints and ascertainable by routine experimentation and optimization to form a number of outputs to the first and second supply potentials because applicant has not disclosed that this limitation is for a particular unobvious purpose, produces an unexpected result, or is otherwise critical, and it appears prima facie that the process would possess utility using another number of outputs. Furthermore, it is well established that the mere repetition or duplication of a prior art process or means to accomplish an expected additive function or result is prima facie obvious absent a disclosure that the process is for a particular unobvious purpose, produces an unexpected result, or is otherwise critical. See, for example, In re Ockert, 114 USPQ 330 (CCPA 1957); In re Schuelke, 96 USPQ 421 (CCPA 1953); In re Hertrich, 73 USPQ 442 (CCPA 1947); Long Mfg. N.C., Inc. v. Condec Corp., 223 USPQ 1213 (DC ENC 1984); St. Regis Paper Company v. Bemis Company, Inc., 193

USPQ 8 (CA 7 1977); Hofschneider Corp. v. Lane et al., doing business as Lane and Co., 71 USPQ 126 (DC WNY 1946).

As to claim 12, one of said first and second metal interconnect structures (fig. 3) is coupled to the first supply potential VDD at a bottom metal layer and the other of said first and second metal interconnect structures is coupled to the second supply potential GND at the bottom metal layer (fig. 3).

As to claim 13, the interconnect structures form a ladder structure (fig. 3).

As to claim 14, the interconnect structures form an offset ladder structure (fig. 7).

Terminal Disclaimer

1. The terminal disclaimer filed on 06/06/2006 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of the patent 6,933,547 has been reviewed and is accepted. The terminal disclaimer has been recorded.

Response to Arguments

2. Applicant's arguments with respect to the pending claims have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

Application/Control Number: 10/697,079

Art Unit: 2814

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Vikki Trinh whose telephone number is (571) 272-1719. The Examiner can normally be reached from Monday-Friday, 9:00 AM - 5:30 PM Eastern Time. If attempts to reach the examiner by telephone are unsuccessful, the Examiner's supervisor, Mr. Wael Fahmy, can be reached at (571) 272-1705. The office fax number is 703-872-9306.

Any request for information regarding to the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Also, status information for published applications may be obtained from either Private PAIR or Public Pair. In addition, status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. If you have questions pertaining to the Private PAIR system, please contact the Electronic Business Center (EBC) at 866-217-9197 (toll free).

Vikki Trinh, Patent Examiner AU 2814

HOKI PHAM PRIMARY EXAMINER Page 5